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CANTOR COLBURN LLP 860-286-0115

NO. 215... P.1

**CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)**Applicant(s): **GARY F. GOTH ET AL.**

Docket No.

POU920030200US1

Serial No.

10/724,347

Filing Date

12/01/2003

Examiner

N/A

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N/A

Invention: **SYSTEM AND METHOD FOR COOLING MULTIPLE LOGIC MODULES****RECEIVED  
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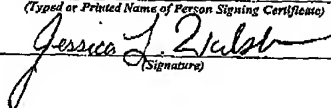
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Preliminary Amendment*(Identify type of correspondence)*is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 703-872-9306)

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NO. 215 P. 2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: GARY F. GOTH ET AL. )  
SERIAL NUMBER: 10/724,847 )  
FILED: December 1, 2003 )  
FOR: SYSTEM AND METHOD FOR COOLING )  
MULTIPLE LOGIC MODULES )

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Commissioner of Patents  
P.O. BOX 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to the examination of the present application, please amend the application as follows:

## IN THE SPECIFICATION:

Please amend paragraph [0043] as follows:

The thermal sensors are compared for miscompare properties and for insanity limits to make sure the temperatures measured are accurate. One sensor is directly sensed by the Module Refrigeration Unit (or MRU) indicated generally at 278 and the other two are read by the power supply feeding the MCM power indicated generally at 280 to insure full redundancy and accuracy of this reading. The MRU reads an MCM hat thermistor sensor directly through its drive card to enable continual monitoring and thermal regulation in case of a cage controller (cc)-failover. MCM hat thermistors that are read by each DCA power supply as well as by the MRU are compared to each other by the MRU and Power Control Code to identify any faulty sensors and eliminate the faulty sensors from consideration generally indicated at 286 in FIG. 4. This insures redundancy of control and cooling status function. The power supply thermistor also serves for thermal protection of the MCMs, dropping power if the temperatures are near damage limits.

Please amend paragraph [0075] as follows:

The pattern to be loaded for speed adjustment purposes such as when going from one cooling state to another is generated by a set of digital I/O input / output lines controlled by the FGAs DIO engines hardware residing on the oscillator card 263, which is a part of connected to the cage controller (cc) hardware. The FGAs DIO engines are digital input / output I/O lines are connected controlled by cage controller code that interface to the PLLs that control the system oscillators 263. They are CSA code driven which is running on the PU Book 0 cage controller (cc). Before changing the PLL pattern due to a change in cooling state, the existing pattern is monitored and the line settings are saved in order to be able to detect and correct interrupted processes to make sure the adjusting processes were not interrupted, by saving the line settings of the current pattern.